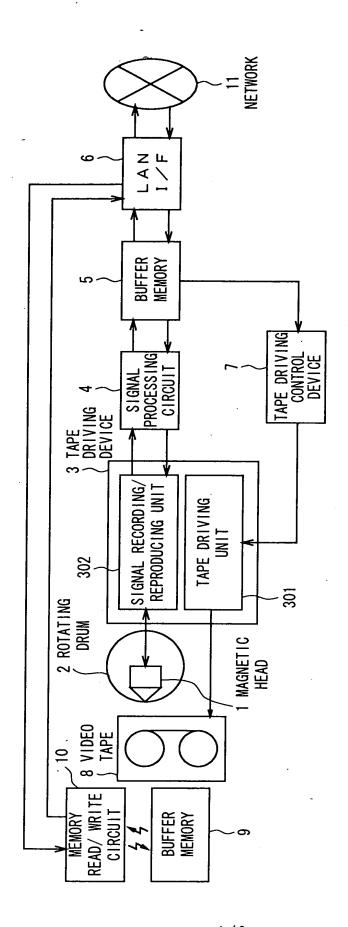
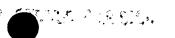
1.



F16.



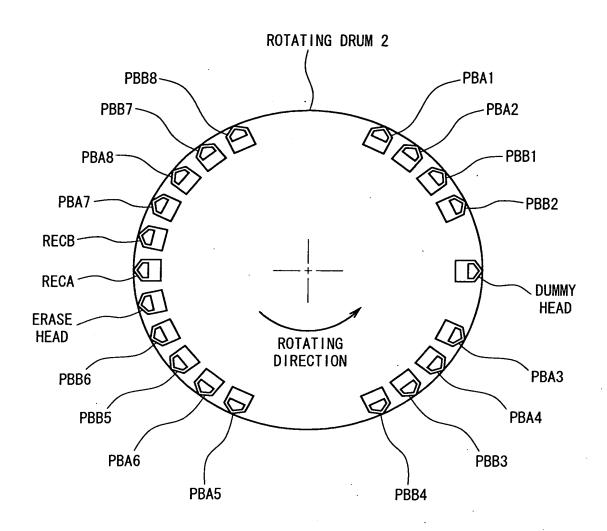
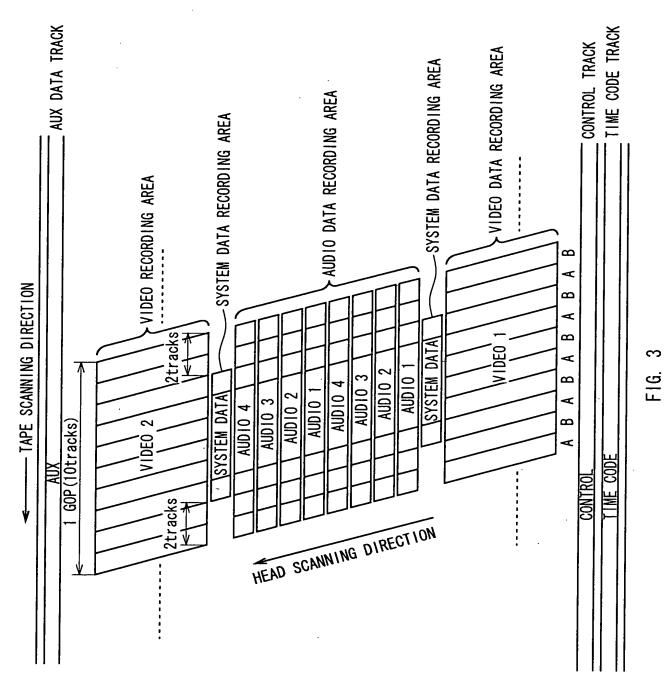
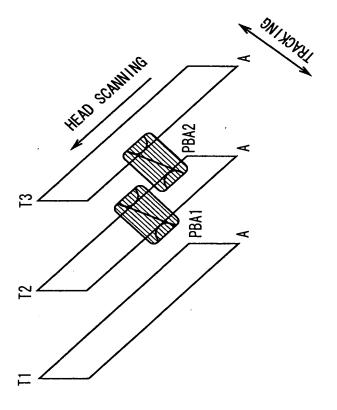


FIG. 2



3/6



F1G. 4

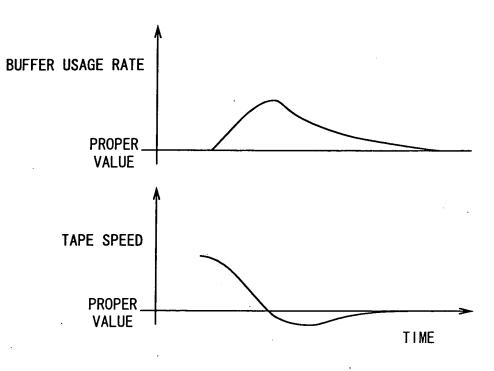
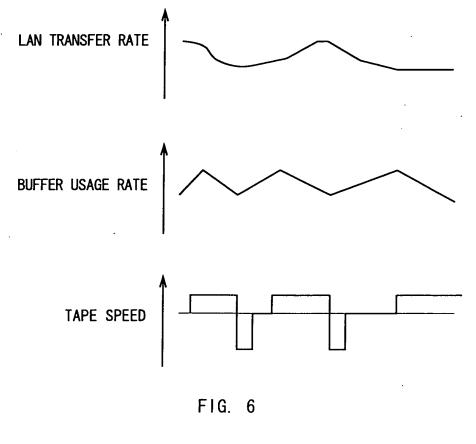


FIG. 5



5/6

## EXPLANATION OF REFERENCE NUMERALS

1... ... tape driving device (driving means), 2... ... signal
processing circuit, 3... ... buffer memory, 4... ... LAN interface
(interface), 5... ... tape driving controller (driving control means),
6... ... buffer memory.